NATIONAL BOARD OF ACCREDITATION

Data Capturing Points of the Program Applied for NBA Accreditation– Tier I/II UG (Engineering) Institute Programs

Program Name : Electronics & Communication Engineering	**Discipline**: Engineering & Technology
Level : Under Graduate	**Tier**: 1
Application No: 12079	**Date of Submission**: 10-09-2026

PART A- Profile of the Institute

A1.Name of the Institute: SRI KRISHNA COLLEGE OF ENGINEERING AND TECHNOLOGY	
Year of Establishment : 1998	Location of the Institute: COIMBATORE
A2. Institute Address:SUGUNAPURAM KUNIAMUTHUR POST	
City:COIMBATORE	State:Tamil Nadu
Pin Code:641008	Website:www.skcet.ac.in
Email:INFO@SKCET.AC.IN	Phone No(with STD Code):0422-2678001
A3. Name and Address of the Affiliating University (if any):	
Name of the University : Anna University, Chennai	City: Chennai
State : Tamil Nadu	Pin Code: 600025
A4. Type of the Institution: Self-Supported Institute	
A5. Ownership Status: Self financing	

A6. Details of all Programs being Offered by the Institution:

- No. of UG programs: **13**
- No. of PG programs: **7**

Table No. A6.1: List of all programs offered by the Institute.

Sr.No.	Discipline	Level of program	Name of the program	Year of Start	Year of Closed	Name of The Department
1	Engineering & Technology	PG	Applied Electronics	2011	2026	Electronics and Communication Engineering
2	Engineering & Technology	UG	Artificial Intelligence and Data Science	2020	--	Artificial Intelligence and Data Science
3	Engineering & Technology	UG	Civil Engineering	2012	--	Civil Engineering
4	Engineering & Technology	PG	Computer Science & Engineering (Integrated)	2019	--	Computer Science and Engineering
5	Engineering & Technology	UG	Computer Science and Business System	2019	--	Computer Science and Business System
6	Engineering & Technology	UG	Computer Science and Design	2022	2025	Computer Science and Design
7	Engineering & Technology	PG	Computer Science and Engineering	2004	--	Computer Science and Engineering
8	Engineering & Technology	UG	Computer Science and Engineering	1998	--	Computer Science and Engineering

9	Engineering & Technology	UG	Computer Science and Engineering (Artificial Intelligence & Machine Learning)	2025	--	Computer Science and Engineering (Artificial Intelligence and Machine Learning)
10	Engineering & Technology	UG	Computer Science and Engineering (Cyber Security)	2022	--	Computer Science and Engineering (Cyber Security)
11	Engineering & Technology	UG	Computer Science and Engineering (Internet of Things)	2025	2026	Computer Science and Engineering (Internet of Things)
12	Engineering & Technology	UG	Electrical and Electronics Engineering	2001	--	Electrical and Electronics Engineering
13	Engineering & Technology	UG	Electronics & Communication Engineering	1998	--	Electronics and Communication Engineering
14	Engineering & Technology	PG	Engineering Design	2012	2026	Mechanical Engineering
15	Engineering & Technology	UG	Information Technology	1998	--	Information Technology
16	Engineering & Technology	UG	Mechanical Engineering	1998	--	Mechanical Engineering
17	Engineering & Technology	UG	Mechatronics Engineering	2000	--	Mechatronics Engineering
18	Engineering & Technology	PG	Power Systems Engineering	2025	2026	Electrical and Electronics Engineering
19	Engineering & Technology	PG	Structural Engineering	2025	--	Civil Engineering
20	Management	PG	Master of Business Administration	2001	--	Management

A7. Programs to be considered for Accreditation vide this Application:

Table No. A7.1: List of programs to be considered for accreditation.

Name of the Department	Having Allied Departments	Name of the Program	Program Level
Electronics and Communication Engineering	Yes	Electronics & Communication Engineering	UG

Table No. A7.2: Allied Department(s) to the Department of the program considered for accreditation as above.
Cluster ID. Name of the Department (in table no. A7.1) Name of allied Departments/Cluster (for table no. A7.1)

Allied Department/Cluster Name	Program Name	Program Level
Mechatronics Engineering	Mechatronics Engineering	UG

PART-B: Program information

B1. Provide the Required Information for the Program Applied For:

Table No. B1: Program details.
A. List of the Programs Offered by the Department:

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY ARROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRAM ACCREDITED	PROGR DURAT
1	Electronics & Communication Engineering	UG	1998 / --	40	Yes	2025	540	2025	AICTE - F.No.Southern/1-44641922094/2025/EOA	Granted accreditation for 3 years for the period (specify period)	2023	2026	5	4

Sanctioned Intake for Last Five Years for the Applied Electronics	
Academic Year	**Sanctioned Intake**
2025-26	540
2024-25	240
2023-24	207
2022-23	207
2021-22	180
2020-21	180

List of the Allied Departments/Cluster and Programs:

SR.NO.	ALLIED DEPARTMENT NAME	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY ARROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRA ACCRED
1	Mechatronics Engineering	Mechatronics Engineering	UG	2000 / --	30	Yes	2022	138	2022	AICTE - F.No. Southern/1-10970061785/2022/EOA	Granted accreditation for 3 years for the period (specify period)	2025	2028	3

Sanctioned Intake for Last Five Years for the Mechatronics Engineering	
Academic Year	**Sanctioned Intake**
2025-26	138
2024-25	138
2023-24	138
2022-23	138
2021-22	120
2020-21	120

B2. Detail of Head of the Department for the program under consideration:

A. Name of the HoD :	Dr. D. Mohana Geetha
B. Nature of appointment:	Regular

C. Qualification:	Ph.D

B3. Program Details

Table No.B3.1: Admission details for the program excluding those admitted through multiple entry and exit points.

Item (Information to be provided cumulatively for all the shifts with explicit headings, wherever applicable)	2025-26 (CAY)	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)	2021-22 (CAYm4)	2020-21 (CAYm5)	2019-20 (CAYm6)
N=Sanctioned intake of the program (as per AICTE /Competent authority)	540	240	207	207	180	180	180
N1=Total no. of students admitted in the 1st year minus the no. of students, who migrated to other programs/ institutions plus no. of students, who migrated to this program	540	240	207	207	180	175	180
N2=Number of students admitted in 2nd year in the same batch via lateral entry including leftover seats	0	20	17	16	13	10	11
N3=Separate division if any	3	9	8	3	6	0	0
N4=Total no. of students admitted in the 1st year via all supernumerary quotas	0	0	0	0	0	0	0
Total number of students admitted in the program (N1 + N2 + N3 + N4) - excluding those admitted through multiple entry and exit points.	543	269	232	226	199	185	191

CAY= Current Academic Year. CAYm1= Current Academic Year Minus 1 CAYm2= Current Academic Year Minus 2. LYG= Last Year Graduate. LYGm1= Last Year Graduate Minus 1. LYGm2= Last Year Graduate Minus 2.

B4. Enrolment Ratio in the First Year

Table No. B4.1: Student enrolment ratio in the 1st year.

Year of entry	N (From Table 4.1)	N1 (From Table 4.1)	N4 (From Table 4.1)	Enrollment Ratio [(N1/N)*100]
2025-26 (CAY)	540	540	0	100.00
2024-25 (CAYm1)	240	240	0	100.00
2023-24 (CAYm2)	207	207	0	100.00

Average [(ER1 + ER2 + ER3) / 3] = 100.00≡ 20.00

B5. Success Rate of the Students in the Stipulated Period of the Program

Table No.B5.1: The success rate in the stipulated period of a program.

Item	(2021-22) LYG	(2020-21) LYGm1	(2019-20) LYGm2
A*= (No. of students admitted in the 1st year of that batch and those actually admitted in the 2nd year via lateral entry, plus the number of students admitted through multiple entry (if any) and separate division if applicable, minus the number of students who exited through multiple entry (if any).	193.00	190.00	191.00
B=No. of students who graduated from the program in the stipulated course duration	177.00	172.00	184.00

Average SR of three batches ((SR_1+ SR_2+ SR_3)/3): 92.68

B6. Academic Performance of the First-Year Students of the Program

Table No.B6.1: Academic Performance of the First-Year Students of the Program.

Academic Performance	CAYm1(2024-25)	CAYm2(2023-24)	CAYm3 (2022-23)
Mean of CGPA or mean percentage of all successful students(X)	7.81	7.85	7.61

Y=Total no. of successful students	248.00	216.00	206.00
Z=Total no. of students appeared in the examination	248.00	216.00	206.00
API [X*(Y/Z)]	7.81	7.85	7.61

Average API[(AP1+AP2+AP3)/3] : 7.76

B7: Academic Performance of the Second Year Students of the Program

Table No.B7.1: Academic Performance of the Second Year Students of the Program.

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 2nd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 2rd year/10)	7.80	7.72	7.55
Y=Total no. of successful students	232.00	222.00	192.00
Z=Total no. of students appeared in the examination	232.00	222.00	193.00
API [X * (Y/Z)]	7.80	7.72	7.51

Average API [(AP1 + AP2 + AP3)/3] : 7.68

B8. Academic Performance of the Third Year Students of the Program

Table No.B8.1: Academic Performance of the Third Year Students of the Program

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 3rd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 3rd year/10)	7.75	7.61	8.29
Y=Total no. of successful students	222.00	191.00	181.00
Z=Total no. of students appeared in the examination	222.00	192.00	182.00
API [X*(Y/Z)]:	7.75	7.57	8.24

Average API [(AP1 + AP2 + AP3)/3] : 7.85

B9. Placement, Higher Studies, and Entrepreneurship

Table No.B9.1: Placement, higher studies, and entrepreneurship details.

Item	LYG (2021-22)	LYGm1(2020-21)	LYGm2(2019-20)
FS*=Total no. of final year students	193.00	190.00	191.00
X=No. of students placed	162.00	152.00	165.00
Y=No. of students admitted to higher studies	10.00	13.00	9.00
Z= No. of students taking up entrepreneurship	3.00	2.00	2.00
Placement Index(P) = (((X + Y + Z)/FS) * 100):	90.67	87.89	92.15

Average Placement Index = (P_1 + P_2 + P_3)/3: 90.24 Placement Index Points:

PART C: Faculty Details in Department and Allied Departments

(Data to be filled in for the Department and Allied Departments)

C1. Faculty details of Department and Allied Departments

Table No.C1: Faculty details in the Department for the past 3 years including CAY

Sr.No	Name of the Faculty	PAN No.	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?
1	Dr. M. Karpagam	XXXXXXX10Q	Ph.D	Anna University	Applied Electronics	01/06/2000	26.2	Lecturer	Professor	02/05/2015	Regular	Yes		No
2	Dr. D. Mohana Geetha	XXXXXXX17B	Ph.D	Anna University	Communication Systems	02/06/2020	6.1	Professor	Professor	02/06/2020	Regular	Yes		Yes
3	Dr. D. Sharmila	XXXXXXX09H	Ph.D	Anna University	Applied Electronics	03/02/2025	1.4	Professor	Professor	03/02/2025	Regular	No	10/06/2026	No
4	Dr. C. Thirumaraiselvi	XXXXXXX03B	Ph.D	Anna University	Applied Electronics	13/06/2007	19.1	Lecturer	Professor	01/10/2018	Regular	Yes		No
5	Dr. V. Nandalal	XXXXXXX50R	Ph.D	Anna University	Communication Systems	02/06/2008	18.2	Lecturer	Professor	01/07/2015	Regular	Yes		No
6	Dr. V.R. Balaji	XXXXXXX19M	Ph.D	Anna University	VLSI Design	27/12/2007	18.8	Lecturer	Professor	09/10/2015	Regular	Yes		No
7	Dr.P. Rajasekar	XXXXXXX34A	Ph.D	Anna University	Applied Electronics	01/07/2025	1	Professor	Professor	01/07/2025	Regular	Yes		No
8	Dr. P. Jayarajan	XXXXXXX85B	Ph.D	Anna University	Applied Electronics	01/07/2025	1	Professor	Professor	01/07/2025	Regular	Yes		No
9	Dr. R. Senthil Ganesh	XXXXXXX06D	Ph.D	Anna University	VLSI Design	02/06/2020	6.1	Associate Professor	Associate Professor	02/06/2020	Regular	Yes		No
10	Dr. T. Joby Titus	XXXXXXX89J	Ph.D	Anna University	Applied Electronics	18/05/2023	3.2	Associate Professor	Associate Professor	18/05/2023	Regular	Yes		No
11	Dr. S. Gowthami	XXXXXXX35Q	Ph.D	Anna University	Applied Electronics	08/08/2024	1.11	Associate Professor	Associate Professor	08/08/2024	Regular	Yes		No
12	Dr. P.K. Poonguzhali	XXXXXXX09P	Ph.D	Anna University	Wireless Communication and Networks	02/06/2025	1.1	Associate Professor	Associate Professor	02/06/2025	Regular	Yes		No
13	Dr. J. Samson Immanuel	XXXXXXX94E	Ph.D	Karunya University	VLSI Design	04/06/2025	1.1	Associate Professor	Associate Professor	04/06/2025	Regular	Yes		No
14	Dr. J. Grace JencyGnanammal	XXXXXXX68J	Ph.D	Karunya University	VLSI Design	09/06/2025	1.1	Associate Professor	Associate Professor	09/06/2025	Regular	Yes		No
15	Dr. R. Remya	XXXXXXX15B	Ph.D	Anna University	Image Processing	09/06/2025	1.1	Associate Professor	Associate Professor	09/06/2025	Regular	Yes		No
16	Dr. S. Sridevi Sathya Priya	XXXXXXX71G	Ph.D	Karunya University	VLSI Design	13/06/2025	0.11	Associate Professor	Associate Professor	13/06/2025	Regular	No	10/06/2026	No
17	Dr. N. Sathishkumar	XXXXXXX14A	Ph.D	Anna University	Communication Systems	02/07/2025	1	Associate Professor	Associate Professor	02/07/2025	Regular	Yes		No

18	Dr H Joseph Prabhakar Williams	XXXXXXX09R	Ph.D	Anna University	Applied Electronics	18/07/2025	1	Associate Professor	Associate Professor	18/07/2025	Regular	Yes		No
19	Dr. M. Ramkumar	XXXXXXX82N	Ph.D	Anna University	Communication Systems	01/06/2013	13.2	Assistant Professor	Associate Professor	03/05/2021	Regular	Yes		No
20	Dr. V. Karthik	XXXXXXX63H	Ph.D	Anna University	Communication Systems	01/06/2012	14.2	Assistant Professor	Associate Professor	01/11/2021	Regular	Yes		No
21	Dr. C. Senthil Kumar	XXXXXXX58C	Ph.D	Anna University	Applied Electronics	01/07/2025	1	Associate Professor	Associate Professor	01/07/2025	Regular	Yes		No
22	Dr. N. Manikandaprabu	XXXXXXX42C	Ph.D	Anna University	Applied Electronics	01/07/2025	1	Associate Professor	Associate Professor	01/07/2025	Regular	Yes		No
23	Dr. D. Devi	XXXXXXX07M	Ph.D	Anna University	Applied Electronics	01/11/2006	19.9	Lecturer	Associate Professor	02/05/2017	Regular	Yes		No
24	Dr. S. M. Asha Banu	XXXXXXX48N	Ph.D	Anna University	Communication Systems	25/05/2016	10.2	Assistant Professor	Assistant Professor		Regular	Yes		No
25	Dr. J.R. Dinesh Kumar	XXXXXXX52D	Ph.D	Anna University	Applied Electronics	15/05/2017	9	Assistant Professor	Assistant Professor		Regular	No	10/06/2026	No
26	Dr. R. Sarathkumar	XXXXXXX73G	Ph.D	Anna University	Applied Electronics	25/06/2012	14.1	Assistant Professor	Assistant Professor		Regular	Yes		No
27	Dr. R. Niranjana	XXXXXXX64J	Ph.D	Anna University	Embedded System Technologies	01/09/2021	4.10	Assistant Professor	Assistant Professor		Regular	Yes		No
28	Dr. D.V. Soundari	XXXXXXX83C	Ph.D	Anna University	VLSI Design	18/01/2013	13.6	Assistant Professor	Assistant Professor		Regular	Yes		No
29	Dr. G. Arun Francis	XXXXXXX20D	Ph.D	Anna University	Antenna Design	05/06/2025	1	Assistant Professor	Assistant Professor		Regular	No	12/06/2026	No
30	Dr. R. Pradeep	XXXXXXX91P	Ph.D	Anna University	Communication Systems	09/06/2025	1.1	Assistant Professor	Assistant Professor		Regular	Yes		No
31	Dr. R.V. Shalini	XXXXXXX95L	Ph.D	Anna University	VLSI Design	11/06/2025	1.1	Assistant Professor	Assistant Professor		Regular	Yes		No
32	Dr.Prema S	XXXXXXX75C	Ph.D	Anna University	VLSI design	02/06/2025	1.1	Assistant Professor	Assistant Professor		Regular	Yes		No
33	Dr Vijayalakshmi B	XXXXXXX13E	Ph.D	Anna University	Communication Systems	02/06/2025	1.2	Assistant Professor	Assistant Professor		Regular	Yes		No
34	Dr.S. Nithya Devi	XXXXXXX58K	Ph.D	Anna University	VLSI Design	02/06/2025	1.1	Assistant Professor	Assistant Professor		Regular	Yes		No
35	Dr.P. Divya	XXXXXXX97P	Ph.D	Anna University	Applied Electronics	02/06/2025	1.1	Assistant Professor	Assistant Professor		Regular	Yes		No
36	Dr.M. Arunkumar	XXXXXXX57C	Ph.D	Anna University	VLSI design	02/06/2025	1.1	Assistant Professor	Assistant Professor		Regular	Yes		No
37	Ms. N. Kalaivani	XXXXXXX03J	M.E.	Anna University	Communication Systems	09/06/2004	22.1	Lecturer	Assistant Professor		Regular	Yes		No

38	Mr. N. Girinath	XXXXXXX12J	M.E.	Anna University	Applied Electronics	01/06/2015	11.2	Assistant Professor	Assistant Professor		Regular	Yes		No
39	Mr. C. Visvesvaran	XXXXXXX14E	M.E.	Anna University	Communication Systems	01/06/2015	11.2	Assistant Professor	Assistant Professor		Regular	Yes		No
40	Ms. G. Saranya	XXXXXXX96N	M.E.	Anna University	Communication Systems	03/06/2015	11.1	Assistant Professor	Assistant Professor		Regular	Yes		No
41	Ms. B. Anish Fathima	XXXXXXX11B	M.E.	Anna University	VLSI Design	08/06/2015	11.1	Assistant Professor	Assistant Professor		Regular	Yes		No
42	Ms. H. Muneera Begum	XXXXXXX92E	M.E.	Anna University	Communication Systems	01/07/2015	11.1	Assistant Professor	Assistant Professor		Regular	Yes		No
43	Dr. S.P. Karthi	XXXXXXX24A	Ph.D	Anna University	Applied Electronics	06/05/2016	10.3	Assistant Professor	Assistant Professor		Regular	Yes		No
44	Ms. N. Nanthini	XXXXXXX29B	M.E.	Anna University	Applied Electronics	22/06/2016	10.1	Assistant Professor	Assistant Professor		Regular	Yes		No
45	Ms. U. Vanitha	XXXXXXX91M	M.Tech	Anna University	Laser & Electro optical	04/06/2018	8.1	Assistant Professor	Assistant Professor		Regular	Yes		No
46	Ms. A. Anie Selva Jothi	XXXXXXX99P	M.Tech	Anna University	Embedded systems technology	10/05/2017	9.2	Assistant Professor	Assistant Professor		Regular	Yes		No
47	Ms. N. Vigneshwari	XXXXXXX74K	M.E.	Anna University	Applied Electronics	16/07/2024	2.1	Assistant Professor	Assistant Professor		Regular	Yes		No
48	Ms. R. Sathyabama	XXXXXXX49M	M.E.	Anna University	Communication Systems	04/06/2025	1.1	Assistant Professor	Assistant Professor		Regular	No	16/07/2026	No
49	Ms. R. Renjitha	XXXXXXX67C	M.E.	Anna University	Communication Systems	02/06/2025	1.1	Assistant Professor	Assistant Professor		Regular	No	08/07/2026	No
50	Ms. S.S. Abinayaa	XXXXXXX77J	M.E.	Anna University	Communication Systems	05/06/2025	1.1	Assistant Professor	Assistant Professor		Regular	Yes		No
51	Ms.B.Vidhya	XXXXXXX91A	M.E.	Anna University	Communication Systems	19/08/2025	0.11	Assistant Professor	Assistant Professor		Regular	Yes		No
52	Dr. S. Sophia	XXXXXXX63G	Ph.D	Anna University	Communication Systems	23/09/2009	15.8	Associate Professor	Professor	01/10/2010	Regular	No	30/05/2025	No
53	Dr. S. Sasi Priya	XXXXXXX81B	Ph.D	Anna University	Communication Systems	05/10/2016	8.7	Professor	Professor	05/10/2016	Regular	No	30/05/2025	No
54	Dr. J. Rejina Parvin	XXXXXXX89J	Ph.D	Anna University	Network Engineering	01/07/2021	3.10	Associate Professor	Associate Professor	01/07/2021	Regular	No	14/05/2025	No
55	Dr. G. Ranjitham	XXXXXXX98D	Ph.D	Anna University	Applied Electronics	04/05/2023	2	Associate Professor	Associate Professor	04/05/2023	Regular	No	30/05/2025	No
56	Dr. S.P. Premnath	XXXXXXX87D	Ph.D	Anna University	Communication Systems	01/02/2018	7.3	Assistant Professor	Assistant Professor		Regular	No	30/05/2025	No
57	Dr. S .P .Cowsigan	XXXXXXX23G	Ph.D	Pondicherry University	Electronics and Communication Engineering	03/05/2023	2	Assistant Professor	Assistant Professor		Regular	No	30/05/2025	No

58	Dr D A Janeera	XXXXXXX50G	Ph.D	Anna University	VLSI Design	23/06/2016	8.11	Assistant Professor	Assistant Professor		Regular	No	30/05/2025	No
59	Dr.J.S.Sujin	XXXXXXX40P	Ph.D	Anna University	Communication Systems	18/04/2024	1.1	Assistant Professor	Assistant Professor		Regular	No	30/05/2025	No
60	Ms. S. Praseetha	XXXXXXX20B	M.E.	Anna University	Communication Systems	15/05/2017	8.2	Assistant Professor	Assistant Professor		Regular	No	31/07/2025	No
61	Ms.M.BenedictTephila	XXXXXXX52P	M.E.	Anna University	Communication Systems	03/06/2019	5.11	Assistant Professor	Assistant Professor		Regular	No	30/05/2025	No
62	Ms. S. Preethi	XXXXXXX55F	M.E.	Anna University	Applied Electronics	03/06/2024	0.11	Assistant Professor	Assistant Professor		Regular	No	30/05/2025	No
63	Dr.K. Priyadharshi ni	XXXXXXX97R	Ph.D	Anna University	VLSI Design	15/05/2017	8	Assistant Professor	Assistant Professor		Regular	No	30/05/2025	No
64	Dr. D. Ruth Anitha Shirley	XXXXXXX12N	Ph.D	Anna University	VLSI Design	15/05/2017	8	Assistant Professor	Assistant Professor		Regular	No	30/05/2025	No
65	Mr. S. Guganesh	XXXXXXX46C	M.E.	Anna University	Applied Electronics	03/06/2024	0.11	Assistant Professor	Assistant Professor		Regular	No	30/05/2025	No
66	Ms.P.Anitha	XXXXXXX41K	M.E.	Anna University	VLSI Design	05/06/2017	9.1	Assistant Professor	Assistant Professor		Regular	Yes		No
67	Dr.S.JayanthiSree	XXXXXXX74D	Ph.D	Anna University	VLSI Design	01/07/2019	4.11	Assistant Professor	Associate Professor	01/02/2021	Regular	No	15/06/2024	No
68	Dr.B.Maruthi Shankar	XXXXXXX97A	Ph.D	Anna University	VLSI Design	03/06/2019	5.1	Associate Professor	Associate Professor	03/06/2019	Regular	No	18/07/2024	No
69	Dr.K.R.Siva Bharathi	XXXXXXX28B	Ph.D	Anna University	Applied Electronics	19/12/2012	11.5	Assistant Professor	Assistant Professor		Regular	No	03/06/2024	No
70	Ms.Roshini I	XXXXXXX51L	M.E.	Anna University	Communication Systems	02/06/2023	1	Assistant Professor	Assistant Professor		Regular	No	03/06/2024	No
71	Ms.JeyaPadmini J	XXXXXXX91E	M.E.	Anna University	VLSI Design	02/06/2023	1	Assistant Professor	Assistant Professor		Regular	No	03/06/2024	No

Table No.C2: Faculty details of Allied Departments for the past 3 years including CAY.

Sr.No	Name of the Faculty	PAN No.	APAAR faculty ID*(if any)	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?
1	Dr. M. Lydia	XXXXXXX55E	XXXXXXXXX531	Ph.D	Karunya University	Electrical & Electronics Engineering	01/06/2020	6.2	Professor	Professor	01/06/2020	Regular	Yes		Yes
2	Dr.T.A.Selvan	XXXXXXX15G	XXXXXXXXX599	Ph.D	Anna University	Mechanical Engineering	06/06/2011	15	Associate Professor	Professor	13/05/2013	Regular	No	11/06/2026	No

3	Dr.D.Pritima	XXXXXXX25C	XXXXXXXXX303	Ph.D	Anna University	Mechanical Engineering	09/07/2020	6.1	Professor	Professor	09/07/2020	Regular	Yes		No
4	Dr.V.Narasimharaj	XXXXXXX52L	XXXXXXXXX231	Ph.D	Anna University	Mechanical Engineering	20/01/2021	5.7	Associate Professor	Associate Professor	20/01/2021	Regular	Yes		No
5	Dr.R.Gopinathan	XXXXXXX47J	XXXXXXXXX635	Ph.D	Anna University	Mechanical Engineering	27/05/2020	6.3	Associate Professor	Associate Professor	27/05/2020	Regular	Yes		No
6	Dr.G.Veerappan	XXXXXXX61Q	XXXXXXXXX542	Ph.D	Anna University	Mechanical Engineering	11/08/2021	5	Associate Professor	Associate Professor	11/08/2021	Regular	Yes		No
7	Dr.S.Balasubramani	XXXXXXX85Q	XXXXXXXXX622	Ph.D	Anna University	Mechanical Engineering	16/09/2021	4.11	Associate Professor	Associate Professor	16/09/2021	Regular	Yes		No
8	Dr.J Indirapriyadharshini	XXXXXXX97P	XXXXXXXXX525	Ph.D	Anna University	Information and Communication Engineering	06/06/2018	8.2	Associate Professor	Associate Professor	06/06/2018	Regular	Yes		No
9	Dr.K. Ananthi	XXXXXXX85A	XXXXXXXXX387	Ph.D	Anna University	Electrical &Electronics Engineering	23/01/2015	11.7	Assistant Professor	Associate Professor	01/06/2018	Regular	Yes		No
10	Dr.L.Feroz Ali	XXXXXXX19K	XXXXXXXXX590	Ph.D	Anna University	Mechanical Engineering	01/06/2018	8.2	Associate Professor	Associate Professor	01/06/2018	Regular	Yes		No
11	Dr. R. Manikandan	XXXXXXX92H	XXXXXXXXX087	Ph.D	Anna University	Mechanical Engineering	17/06/2021	5.2	Associate Professor	Associate Professor	17/06/2021	Regular	Yes		No
12	Dr. N. Mithran	XXXXXXX70J	XXXXXXXXX497	Ph.D	SASTRA University	Mechanical Engineering	20/01/2015	11.7	Assistant Professor	Associate Professor	10/06/2021	Regular	Yes		No
13	Dr. J. Justin Maria Hillary	XXXXXXX15D	XXXXXXXXX779	Ph.D	Anna University	Mechanical Engineering	03/01/2022	4.7	Associate Professor	Associate Professor	03/01/2022	Regular	Yes		No
14	Dr M Bhuvaneswari	XXXXXXX95L	XXXXXXXXX202	Ph.D	Anna University	Information and Communication Engineering	11/09/2013	12.11	Assistant Professor	Assistant Professor		Regular	Yes		No
15	Dr.S.Dinesh	XXXXXXX40Q	XXXXXXXXX342	Ph.D	Anna University	Mechanical Engineering	01/08/2023	3	Assistant Professor	Assistant Professor		Regular	Yes		No
16	Dr. S. Madhankumar	XXXXXXX20Q	XXXXXXXXX485	Ph.D	Anna University	Mechanical Engineering	02/06/2017	9	Assistant Professor	Assistant Professor		Regular	No	11/06/2026	No
17	Dr.S.Raja	XXXXXXX48Q	XXXXXXXXX903	Ph.D	Anna University	Mechanical Engineering	11/06/2025	0.11	Associate Professor	Associate Professor	11/06/2025	Regular	No	30/05/2026	No
18	Ms. S. Kannaki	XXXXXXX22Q	XXXXXXXXX431	M.E.	Anna University	Power Electronics and Drives	05/06/2014	12.2	Assistant Professor	Assistant Professor		Regular	Yes		No
19	Ms.S.Nithya Priya	XXXXXXX10J	XXXXXXXXX445	M.E.	Anna University	Power Electronics and Drives	01/06/2012	14.2	Assistant Professor	Assistant Professor		Regular	Yes		No
20	Mr. P. Sathish Kumar	XXXXXXX80L	XXXXXXXXX002	M.E.	Anna University	CAD/CAM	02/06/2014	12.2	Assistant Professor	Assistant Professor		Regular	Yes		No

21	Mr. M. Aravind	XXXXXXX73J	XXXXXXXXX080	M.E.	Anna University	Manufacturing	23/06/2014	12.1	Assistant Professor	Assistant Professor		Regular	Yes		No
22	Ms.R.Priyadharshini	XXXXXXX74Q	XXXXXXXXX150	M.E.	Anna University	Process Control and Instrumentation	22/06/2015	11.1	Assistant Professor	Assistant Professor		Regular	Yes		No
23	Mr.S.Panneerselvam	XXXXXXX83P	XXXXXXXXX272	M.E.	Anna University	Engineering Design	01/06/2015	11	Assistant Professor	Assistant Professor		Regular	No	11/06/2026	No
24	Mr.M.Vigneshwaran	XXXXXXX11J	XXXXXXXXX050	M.E.	Anna University	CAD/CAM	17/06/2016	10.1	Assistant Professor	Assistant Professor		Regular	Yes		No
25	Mr. S. Logesh	XXXXXXX53E	XXXXXXXXX585	M.E.	Anna University	Product Design and Development	09/06/2022	4.1	Assistant Professor	Assistant Professor		Regular	Yes		No
26	Mr T Vignesh	XXXXXXX55P	XXXXXXXXX089	M.E.	Anna University	Mechatronics Engineering	01/06/2017	8	Assistant Professor	Assistant Professor		Regular	No	30/05/2025	No

C2. Student-Faculty Ratio (SFR)

No. of UG(Engineering) programs in Department including allied departments/ clusters (UGn):
UG1=1st UG program
UGn=nth UG program
B= No. of Students in UG 2nd year (ST)
C= No. of Students in UG 3rd year (ST)
D= No. of Students in UG 4th year (ST)
No. of PG (Engineering) programs in Department including allied departments/ clusters (PGm):
PG1=1st PG program.
PGm=mth PG program
A= No. of Students in PG 1st year
B= No. of Students in PG 2nd year
Student Faculty Ratio **(SFR)** = S/F
S= No. of students of all programs in the Department including all students of allied departments/clusters.
No. of students (ST)=Sanctioned Intake (SA)+ Actual admitted students via lateral entry including leftover seats (L) if any (limited to 10 % of SA)
Students who admitted under supernumerary quotas (SNQ, EWS, etc) will not be considered in calculating SFR value. Those students are exempted.
F=Total no. of regular or contractual faculty members (Full Time) in the Department, including allied departments/clusters (excluding first year faculty (The faculty members who have a 100% teaching load in the first-year courses)).

No. of UG Programs in the Department1 No. of PG Programs in the Department1

Table No.C2.1: Student-faculty ratio.

Description	**CAY(2025-26)**	**CAYm1 (2024-25)**	**CAYm2 (2023-24)**
UG1.B	260	224	223
UG1.C	224	223	193
UG1.D	223	193	190
UG1: Electronics & Communication Engineering	**707**	**640**	**606**
UG2.B	149	150	151
UG2.C	150	151	132
UG2.D	151	132	132
UG2: Mechatronics Engineering	**450**	**433**	**415**

Description	CAY(2025-26)	CAYm1 (2024-25)	CAYm2 (2023-24)
PG1.A	12	9	9
PG1.B	9	9	18
PG1: Applied Electronics	**21**	**18**	**27**
DS=Total no. of students in all UG and PG programs in the Department	728	658	633
AS=Total no. of students of all UG and PG programs in allied departments	450	433	415
S=Total no. of students in the Department (DS) and allied departments (AS)	**S1=** 1178	**S2=** 1091	**S3=** 1048
DF=Total no. of faculty members in the Department	52	42	42
AF= Total no. of faculty members in the allied Departments	25	25	25
F=Total no. of faculty members in the Department (DF) and allied Departments (AF)	**F1=** 77	**F2=** 67	**F3=** 67
FF=The faculty members in F who have a 100% teaching load in the first-year courses	4	4	3
Student Faculty Ratio (SFR)=S/(F-FF)	**SFR1= 15.50**	**SFR2= 16.53**	**SFR3= 16.38**
Average SFR for 3 years	**SFR= 16.14**		

C3. Faculty Qualification

- Faculty qualification index (FQI) = 2.5 * [(10X +4Y)/RF] where
- X=No. of faculty members with Ph.D. degree or equivalent as per AICTE/UGC norms.
- Y=No. of faculty members with M. Tech. or ME degree or equivalent as per AICTE/ UGC norms.
- RF=No. of required faculty in the Department including allied Departments to adhere to the 20:1 Student-Faculty ratio, with calculations based on both student numbers and faculty requirements as per section C2 of this documents: (RF=S/20).

Table No.C3.1: Faculty qualification.

Year	X	Y	RF	FQ = 2.5 x [(10X + 4Y) / RF)]
2025-26(CAY)	51	29	58.00	26.98
2024-25(CAYm1)	37	33	54.00	23.24
2023-24(CAYm2)	34	33	52.00	22.69

C4. Faculty Cadre Proportion

- Faculty Cadre Proportion is 1(RF1): 2(RF2): 6(RF3)
- RF1= No. of Professors required = 1/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per C2 of this documents:.
- RF2= No. of Associate Professors required = 2/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:.
- RF3= No. of Assistant Professors required = 6/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:.
- Faculty cadre and qualification and experience should be as per AICTE/UGC norms.

Table No.C4.1: Faculty cadre proportion details.

Year	Professors		Associate Professors		Assistant Professors	
	Required RF1	Available AF1	Required RF2	Available AF1	Required RF3	Available AF3
2025-26	7.00	11.00	13.00	26.00	39.00	40.00
2024-25	6.00	10.00	12.00	17.00	36.00	40.00

2023-24	6.00	10.00	12.00	18.00	35.00	39.00
Average	RF1=6.33	AF1=10.33	RF2=12.33	AF2=20.33	RF2=36.67	AF2=39.67

C5. Visiting/Adjunct Faculty/Professor of Practice

Table No. C5.1: List of visiting/adjunct faculty/professor of practice and their teaching and practical loads.

(CAYm1)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Dr Sivakumar R	Chief Executive	Sai Vortex Energy Solutions	23EC403 Embedded C++	30.00
2	Dr Sivakumar R	Chief Executive	Sai Vortex Energy Solutions	23EC302 Operating System for Electronic Devices	30.00
3	Ms.B.Jyothe Kritica	Research Engineer	Science and technology research organisation pvt Ltd	23EC601 Green Communication and Networks	30.00
4	Ms.B.Jyothe Kritica	Research Engineer	Science and technology research organisation pvt Ltd	22EC503 Wireless Communication	30.00

(CAYm2)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Dr. M. Uma Parvathi	Director & Quality Manager	Gayathiri Calibration Services	22EC402Analog and Digital Communication	30.00
2	Dr. M. Uma Parvathi	Director & Quality Manager	Gayathiri Calibration Services	20EC701 Microwave and Optical Communication	30.00
3	Mr.Vignesh	Technical Engineer	Technical Engineer Techquad Solutions Pvt. Ltd.,	22EC403 Analog Integrated Circuits	30.00
4	Mr.Vignesh	Technical Engineer	Technical Engineer Techquad Solutions Pvt. Ltd.,	22EC301 Electronic Circuits	30.00

(CAYm3)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Dr. M. Uma Parvathi	Director & Quality Manager	Gayathiri Calibration Services	21EC403 Analog and Digital Communication	50.00
2	Ms. N. Malliga Devi	Director	Confident Engineering Pvt Ltd,	22EC201 Electron Devices	60.00
3	Mr.Vignesh	Technical Engineer	Techquad Solutions Pvt. Ltd	21EC302 Electronic Circuits	60.00

C6. Academic Research

Table No. C6.1: Faculty publication details.

S.No.	Item	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)
1	No. of peer reviewed journal papers published	48	39	36
2	No. of peer reviewed conference papers published	77	68	61
3	No. of books/book chapters published	8	7	5

C7. Sponsored Research Project

Table No. C7.1: List of sponsored research projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.Devi D		Electronics and Communication Engineering	Design and development of a real time hospital management and patient monitoring system	Tamilnadu skill development corporation	1Year	0.10
Ms. Nanthini N		Electronics and Communication Engineering	Smart vehicle safety and monitoring system for enhanced road safety	Tamilnadu skill development corporation	1Year	0.10
Ms.Vigneshwari N		Electronics and Communication Engineering	Automatic bus fare collection system	Tamilnadu skill development corporation	1Year	0.10
Dr.Niranjana R		Electronics and Communication Engineering	Ultrasonic Smart Assistive Cane	Tamilnadu State Council for Science and Technology	1Year	0.08
Dr.Ramkumar		Electronics and Communication Engineering	Drone Service, Telecom IoT.	NSDC, PMKVY	2 Years	7.98
Dr.Ramkumar	Dr. Shanmuga Raju S	Electronics and Communication Engineering	Idea Lab	AICTE	2 Years	10.00
Mr. Visveswaran C		Electronics and Communication Engineering	Solar Powered IoT-Enabled Precision Irrigation System for Sustainable Water Management in Agriculture	Unnat Bharat Abhiyan	1 Year	1.00
						Amount received (Rs.):19.36

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr. Sasipriya S		Electronics and Communication Engineering	Smart Integration of advanced surveillance systems in gas sensing using drones	Niral Thiruvizha 2024	1 Year	0.10
Dr. Balaji V R		Electronics and Communication Engineering	Fireguard: Deep CNN video surveillance for efficient fire detection	Niral Thiruvizha 2024	1 Year	0.10
Dr. Sophia S		Electronics and Communication Engineering	Enhancing Soft Know-How and Technical Competences for SC/ST Students	Niral Thiruvizha 2024	3 Years	1.81
Dr.Ramkumar		Electronics and Communication Engineering	Drone Service, Telecom IoT.	NSDC, PMKVY	2 Years	10.32
						Amount received (Rs.):12.33

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.Albert Raj A		Electronics and Communication Engineering	Tire Life Prediction System using Convolutional Neural Network	TNSCST	1 Year	0.08
Dr.Albert Raj A		Electronics and Communication Engineering	Environment Friendly and Efficient Honey Harvest Alert System for Bee Farming	IEEE	2 Years	0.35
						Amount received (Rs.):0.43

Total Amount (Lacs) Received for the Past 3 Years: 32.12

Note*:

- Only sponsored research projects will be considered. Infrastructure-based projects will not be considered here.

C8. Consultancy Work

Table No. C8.1: List of consultancy projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr Karpagam M	Dr Senthil Ganesh R & Dr Karthi S P	ECE	Development of an AIoT Based Smart Energy Monitoring and Optimization System for Industrial Facilities	Jayef Engineering, Coimbatore	1 year	3.55
Dr Balaji V R	Dr Joby Titus T & Dr Devi D	ECE	Design and Development of an Embedded Vision Based Automated Inspection System	Capecom Solutions Pvt Ltd, Nagercoil	1 year	3.10
Dr Sharmila D	Dr Asha Banu S M & Ms Nanthini N	ECE	Design and Development of a Wearable Antenna Based Wireless Health and Safety Monitoring System	Heterogeneous Communication Technologies Private Limited, Chennai	1 year	4.20
						Amount received (Rs.):10.85

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr Thirumarai Selvi C	Dr Devi D & Ms Kalaivani N	ECE	Design and Development of an AI-Based Wireless Signal Classification and Interference Detection System	Entool Industrial Services, Coimbatore	1 year	4.85
Dr Nandalal V	Dr Ranjitham G & Dr Karthik V	ECE	FPGA-Based Low-Power AI Accelerator for Edge Computing Applications	Capecom Solutions Pvt Ltd, Nagercoil	1 year	3.81
						Amount received (Rs.):8.66

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr Albert Raj A	Dr Sarath Kumar R & Dr Premnath S P	ECE	Polar encoder with Wavelet pipelining Architecture for 5G Applications	General Electrical Onshore, Kerala	1 year	1.00
Dr Joby Titus T	Dr Niranjana R & Ms Anie Selva Jothi A	ECE	Design of an Embedded Intelligent Safety Monitoring System for Industrial Applications	Franklin Appliances, Coimbatore	1 year	4.73
						Amount received (Rs.):5.73

Total amount (Lacs) received for the past 3 years: 25.24

Note*:

- Only consultancy projects will be considered. Infrastructure-based projects will not be considered here.

C9. Institution Seed Money or Internal Research Grant to its Faculty for Research Work

Table No. C9.1: List of faculty members received seed money or internal research grant from the Institution.

(CAYm1)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr. Karthik V	Growing Degree Days and Crop Growth Rate Production	1 Year	1.00	1.00	Hardware
Dr. Cowsigan S P	Leaf Disease Prediction and Suggesting Pesticides for Users	1 Year	1.02	1.00	Hardware
Dr. Senthil Ganesh R	IoT-Based Predictive Maintenance System for Industrial Equipment	1 Year	1.03	1.03	Hardware
Dr. Karpagam M	Wearable IoT Device for Continuous Health Parameter Monitoring	1 Year	1.03	1.03	Hardware
Ms. Nanthini N	AI-Based Energy Consumption Prediction for Smart Buildings	1 Year	1.05	1.05	Hardware
			Amount received (Rs.): 5.13		

(CAYm2)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr.S.Saipriya	ENHANCING EARLY DETECTION AND PREVENTION DEVICE FOR CHRONIC KIDNEY DISEASE	1 Year	1.48	1.38	Hardware
Dr.D.Mohanageetha	AI BASED DRONE FOR SUSTAINABLE HONEYCOMB EXTRACTION	1 Year	1.20	1.12	Hardware
Dr. Dinesh Kumar J R	A Home-Based Cognitive Enhancement Program for Children	1 Year	1.38	1.39	Hardware
			Amount received (Rs.): 4.06		

(CAYm3)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr. A. Albert Raj	IoT-Based Smart Shoe Design for Healthy Foot for teachers	1 Year	1.00	0.96	Hardware
			Amount received (Rs.): 1.00		

Total amount (Lacs) received for the past 3 years : 10.19

PART D: Laboratory Infrastructure in the Department

(Data to be filled in for the Department)

D1. Adequate and Well-Equipped Laboratories, and Technical Manpower

Table No.D1.1: List of laboratories and technical manpower.

Sr. No	Name of the Laboratory	Number of students per set up(Batch Size)	Name of the Important Equipment	Weekly utilization status(all the courses for which the lab is utilized)	Technical Manpower Support		
					Name of the Technical staff	Designation	Qualification
1	Embedded Systems and VLSI Laboratory (89.04 m2)	1	•Advanced ARM IOT development Kit • IoT Trainer Kit • Cortex M7 Development board •ARM 7 Development board LPC2148 Development	30	Mr. D. Velmurugan	Lab Technician	B.Sc. CSE
2	Digital Signal Processing Laboratory Laboratory (89.04 in m2)	1	•TMS320C6713 based DSP starter kit with code composer studio • TMS320C50 Based DSP Trainer kit MATLAB Software TMS 320C5416 DSP	30	Mr. N.Manikandan	Lab Technician	DECE
3	RF, Microwave & Optical Laboratory (137.02 m2)	3	•Microwave Test Benches • Advanced Fiber Optic Trainer Kit with BER module • Spectrum Analyzer RF Modular Set Advanced Antenna Training	18	Ms. S. Ramya	Lab Technician	DECE
4	Communication Laboratory (224.82 m2)	3	•Digital Storage Oscilloscope, • Cathode Ray Oscilloscope, • Function Generator • Pulse Generator DC Regulated Power Supply	27	Ms. R.Lakshmi	Lab Technician	DCP, HDCA, ADCHN
5	Electronics Laboratory (224.82 m2)	3	•Digital Storage Oscilloscope, • Cathode Ray Oscilloscope, • Function Generator • Pulse Generator DC Regulated Power supply IC Trainer	27	Ms. D. Ramya	Lab Technician	DECE
6	Microprocessor Laboratory	3	•8086 Microprocessor Trainer Kit • 8051 Microcontroller Trainer Kit ADC & DAC Interface 8051	24	Ms. V. Thilagavathi	Lab Technician	D.C.P
7	Digital Laboratory (89.91 in m2)	3	•CRO(0-20) MHz,(0-30)MHz • Function Generator (0-3)MHz • Power supply (0-32)V	27	Mr. G. S. Sathishkumar	Lab Technician	B.E.

D2. Safety Measures in Laboratories

Table No. D2.1: List of various safety measures in laboratories.

Sr. No	Laboratory Name	Safety Measures

1	Embedded Systems and VLSI Laboratory	• Proper earthing and regulated power supply for all development boards and PCs. • Verify voltage polarity and connections before powering ARM, LPC2148 and 8051 boards. • Avoid short circuits and disconnect power before changing jumper/circuit connections. • Use ESD precautions while handling microcontrollers, development boards and electronic components. • Ensure proper earthing and regulated power supply for FPGA/VLSI development kits. • Verify supply voltage, polarity and connections before powering FPGA boards. • Switch OFF power before changing jumper wires, interfacing modules or board connections. • Follow ESD precautions while handling FPGA boards, ICs and electronic components. • Specific Safety Rules in the form of precautions, guidelines, Do's and Don'ts are displayed in the Laboratory. • First Aid Box and Fire Extinguishers are placed at the immediate accessible spot for safety
2	Digital Signal Processing Laboratory	• Ensure proper earthing and regulated power supply for DSP kits and codec interfaces. • Verify all signal, power and codec connections before switching ON the equipment. • Switch OFF power before changing cables, connectors or hardware interfaces. • Follow ESD precautions while handling DSP boards, processors and LiDAR equipment. • Specific Safety Rules in the form of precautions, guidelines, Do's and Don'ts are displayed in the Laboratory. • First Aid Box and Fire Extinguishers are placed at the immediate accessible spot for safety
3	RF, Microwave & Optical Laboratory	• Ensure proper earthing and regulated power supply for all RF/microwave instruments. • Follow safe operating procedures for microwave sources, RF modules and antenna systems. • Avoid direct exposure to active RF/microwave sources and maintain safe operating distances. • Handle optical-fiber equipment carefully; avoid direct expo-sure to laser/optical sources. • Specific Safety Rules in the form of precautions, guidelines, Do's and Don'ts are displayed in the Laboratory. • First Aid Box and Fire Extinguishers are placed at the immediate accessible spot for safety
4	Microprocessor Laboratory	• Ensure proper earthing and regulated power supply for all trainer and development kits. • Verify voltage, polarity and interface connections before powering the kits. • Switch OFF the supply before changing connections, programming cables or interface modules. • Follow ESD precautions while handling 8086, 8051, PIC, MSP430, ARM and Arduino boards. • Specific Safety Rules in the form of precautions, guidelines, Do's and Don'ts are displayed in the Laboratory. • First Aid Box and Fire Extinguishers are placed at the immediate accessible spot for safety
5	Digital Laboratory	• Ensure proper earthing and safe operation of CRO, function generator and DC power supply. • Verify voltage, polarity and frequency settings before connecting the circuit. • Switch OFF the power supply before changing or removing circuit connections. • Avoid short circuits and do not exceed the rated voltage/current of components and equipment. • Specific Safety Rules in the form of precautions, guidelines, Do's and Don'ts are displayed in the Laboratory. • First Aid Box and Fire Extinguishers are placed at the immediate accessible spot for safety
6	Communication Laboratory	• Ensure proper earthing and regulated power supply for all instruments. • Verify voltage, polarity and instrument range before making connections. • Switch OFF the power supply before modifying or disconnect-ing circuits. • Handle CRO/DSO probes, function/pulse generators and ICs carefully to prevent damage. • Specific Safety Rules in the form of precautions, guidelines, Do's and Don'ts are displayed in the Laboratory. • First Aid Box and Fire Extinguishers are placed at the immediate accessible spot for safety
7	Electronics Laboratory	• Proper earthing and regulated power supply for all instruments. • Verify voltage, polarity and measurement range before circuit connections. • Switch OFF power before modifying or disconnecting circuit connections. • Handle IC trainer kits, CRO/DSO probes and signal generators carefully. • Specific Safety Rules in the form of precautions, guidelines, Do's and Don'ts are displayed in the Laboratory. • First Aid Box and Fire Extinguishers are placed at the immediate accessible spot for safety

D3. Project Laboratory/Research Laboratory

The departments project and research facilities provide students, faculty and research scholars with an environment for design, simulation, prototyping, testing and validation. The facilities support mini projects, major projects, postgraduate projects and research activities. Students can progress from problem definition and simulation to prototype development and experimental verification.

Table 7.5.1 List of project laboratory/research laboratory /Centre of Excellence.

S.No.	Name of the Laboratory/Centre of Excellence
1.	Project Lab
2.	Research Lab
3.	**Centre of Excellence** • AICTE-AVPL Centre of Excellence for Drones • All India Council for Robotics and Automation (AICRA) • Athes Centre of Excellence in Defence Technology • Centre for Vehicle Fabrication • AICTE Idea Lab • Capgemini Technologies Services India Limited • Centre of Excellence in VLSI Design and Semiconductor Technology - Chip to startup -

7.5.1 List of project laboratory/research laboratory Facilities and Outcome

Table 7.5.2 Facilities available in project laboratory/research laboratory

S.No.	Lab Name	Facilities available	Outcome
1	Project Lab	**IOT Module:** IoT Trainer Kit , IoT Development Kit ,Zigbee Bluetooth Development kit and Bluetooth Low energy development kit and Wifi ssensor nodes	• Student mini and major projects • Hackathon and innovation prototypes • IoT and wireless communication prototypes • UG/PG research projects

2	Research Lab	**Electronic Design Automation (EDA) Tools** Synopsys, Ansys, Keysight, Xilinx, and Silvaco **PCB and Antenna Prototype Machine :** ETS – PCBMATE 300W Prototype and Antenna fabrication machine & Workstation, Tiny Vector Network Analyzer ANSYS Academic Research HF Software	• Mini Projects • Final year Projects and Research • Research Publications • Patent Publications

7.5.2 Centre of Excellence:

1.Lab Name : Centre of Excellence in Embedded system Powered by Capgemini

Capgemini Engineering, A global leader in consulting, technology services anddigital engineering in association with Sri Krishna College of Engineering and Technology opened a Centre of Excellence (CoE) in Embedded Systems. The CoE is set up with an aim to nurture young talents who are interested in pursuing a course on Embedded systems.

Capgemini Engineering helps institutions thrive in an open and rapidly changing world and together aiming to provide a new opportunity for students to learn and work on emerging areas of technology.

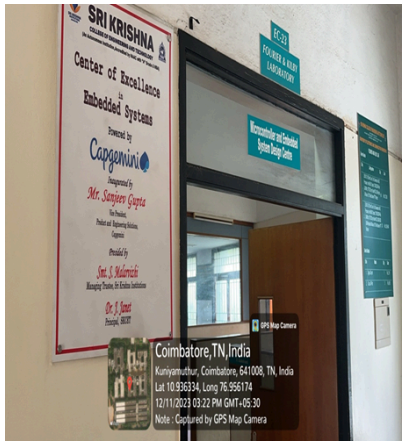
As part of the association, few faculty members of Sri Krishna College of Engineering and technologywill be undergoing training course at Capgemini Engineering campus for enrolling the aspirants of SKCET.

Additionally, Capgemini Engineeringwill nominate its subject matter experts to give out industry-oriented valuable inputs. Students will have the opportunity to work on real-time cases that will expose them to actual work scenarios. The Capgemini Engineering team will mentor and review their work to ensure they are industry-ready by the time they graduate. Students will also be given an opportunity to intern at Capgemini Engineering offices along with pre-placement offers.

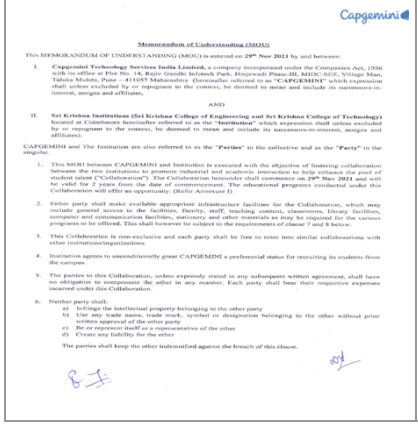
This CoE commits to provide project ideas and remotely mentor students to execute projects at the Institute’s Campus. It will also openup opportunities for deserving students and faculty to participate in Capgemini’s Tech-Fiesta. The course curriculum is framed in-line with the concurrent Industry needs. Capgemini provides pre joining learning opportunities (e.g. ADAPT) to Campus selects by means of this CoE.

This CoE enables us to work together more closely towards our common goal- training and in molding the students to be industry-ready professionals. Through this CoE, we will pursue many significant activities, such as the promotion of study opportunities, assistance to support study and educational opportunities, to those seeking information in the area of education and training for faculty and students.

Capegemini Engineering helps institutions thrive in an open and rapidly changing world and together aiming to provide a new opportunity for students to learn and work on emerging areas of technology.







Capgemini

Memorandum of Understanding (MOU)

This MEMORANDUM OF UNDERSTANDING (MOU) is entered on 29th Nov 2021 by and between:

I. **Capgemini Technology Services India Limited,** a company incorporated under the Companies Act, 1956 with its office at Plot No. 14, Rajiv Gandhi Infotech Park, Hinjewadi Phase-III, MIDC-SEZ, Village Man, Taluka Mulshi, Pune – 411057 Maharashtra (hereinafter referred to as **"CAPGEMINI"** which expression shall unless excluded by or repugnant to the context, be deemed to mean and include its successors-in-interest, assigns and affiliates,

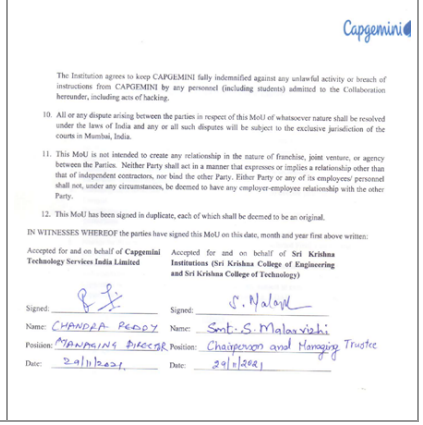
AND

II. **Sri Krishna Institutions (Sri Krishna College of Engineering and Sri Krishna College of Technology)** located at Coimbatore hereinafter referred to as the **"Institution"** which expression shall unless excluded by or repugnant to the context, be deemed to mean and include its successors-in-interest, assigns and affiliates).

CAPGEMINI and The Institution are also referred to as the **"Parties"** to the collective and as the **"Party"** in the singular.

1. This MOU between CAPGEMINI and Institution is executed with the objective of fostering collaboration between the two institutions to promote industrial and academic interaction to help enhance the pool of student talent ("Collaboration"). The Collaboration hereunder shall commence on 29th Nov 2021 and will be valid for 2 years from the date of commencement. The educational programs conducted under this Collaboration will offer an opportunity: (Refer Annexure I)
2. Either party shall make available appropriate infrastructure facilities for the Collaboration, which may include physical access to the facilities, faculty, staff, teaching content, classrooms, library facilities, computer and communication facilities, stationery and other materials as may be required for the various programs to be offered. This shall however be subject to the requirements of clause 7 and 8 below.
3. This Collaboration is non-exclusive and each party shall be free to enter into similar collaborations with other institutions/organizations.
4. Institution agrees to unconditionally grant CAPGEMINI a preferential status for recruiting its students from the campus.
5. The parties to this Collaboration, unless expressly stated in any subsequent written agreement, shall have no obligation to compensate the other in any manner. Each party shall bear their respective expenses incurred under this Collaboration.
6. Neither party shall:
   a) Infringe the intellectual property belonging to the other party
   b) Use any trade name, trade mark, symbol or designation belonging to the other without prior written approval of the other party
   c) Be or represent itself as a representative of the other
   d) Create any liability for the other

   The parties shall keep the other indemnified against the breach of this clause.



Capgemini

The Institution agrees to keep CAPGEMINI fully indemnified against any unlawful activity or breach of instructions from CAPGEMINI by any personnel (including students) admitted to the Collaboration hereunder, including acts of hacking.

10. All or any dispute arising between the parties in respect of this MoU of whatsoever nature shall be resolved under the laws of India and any or all such disputes will be subject to the exclusive jurisdiction of the courts in Mumbai, India.
11. This MoU is not intended to create any relationship in the nature of franchise, joint venture, or agency between the Parties. Neither Party shall act in a manner that expresses or implies a relationship other than that of independent contractors, nor bind the other Party. Either Party or any of its employees/ personnel shall not, under any circumstances, be deemed to have any employer-employee relationship with the other Party.
12. This MoU has been signed in duplicate, each of which shall be deemed to be an original.

IN WITNESSES WHEREOF the parties have signed this MoU on this date, month and year first above written:

Accepted for and on behalf of **Capgemini Technology Services India Limited**	Accepted for and on behalf of **Sri Krishna Institutions (Sri Krishna College of Engineering and Sri Krishna College of Technology)**
Signed:	Signed: S. Malavi
Name: CHANDRA REDDY	Name: Smt. S. Malavizhi
Position: MANAGING DIRECTOR	Position: Chairperson and Managing Trustee
Date: 2-9/11/2021	Date: 29/11/2021

Program Outcomes (POs)	Program Specific Outcomes (PSOs)	Sustainability Development Goal (SDG)
PO1, PO2, PO3, PO4, PO5, PO6, PO7, PO8, PO9, PO10, PO11	PSO1, PSO2	SDG 4, SDG 8, SDG 9, SDG 11

Products Developed :

- Intelligent Traffic management system for Emergency vehicles using GPS and GSM
- IoT Based Air quality system
- Timer based automation in lights
- Biometrics - Based VotingMachine Combined
- With Result Analysis Software
- Smart Medicine Reminder

Expected Outcome :

· Participation in **hackathons, project exhibitions and innovation competitions** using developed prototypes

· Student projects and working prototypes

2.Lab name: AVPL- DRONE CENTRE OF EXCELLENCE

Program Outcomes (POs)	Program Specific Outcomes (PSOs)	Sustainability Development Goal (SDG)
PO1, PO2, PO3, PO4, PO5, PO6, PO7, PO8, PO9, PO10, PO11	PSO1, PSO2	SDG 4, SDG 8, SDG 9, SDG 11

The Drone Centre of Excellence (CoE) established by AVPL International is an advanced training and skill development facility aimed at promoting drone technology education, research, and entrepreneurship. The centre focuses on providing hands-on training in drone operation, maintenance, data analysis, and applications in sectors such as agriculture, defence, infrastructure and surveying.

The CoE is designed to develop skilled drone pilots and technicians through DGCA-approved certification programs and industry-oriented courses. It also supports innovation through incubation facilities, industry collaboration, and practical exposure to real-time drone applications. Through this centre, AVPL aims to create employment opportunities, promote start-ups, and empower students and rural youth with emerging technology skills. The initiative also supports the development of “drone entrepreneurs” by providing technical training and business guidance.



· Agricultural Crop Monitoring Drone – Drone-based monitoring of crop health and field conditions.

· Precision Agriculture Drone – Aerial system for targeted spraying, crop assessment and agricultural data collection.

· Disaster Management Drone – Drone prototype for monitoring flood-, fire- or disaster affected areas.

· Search and Rescue Drone – Aerial platform for locating people in difficult-to-access areas.

Expected Outcomes

· Student projects and working prototypes

· Drone-based hackathon and innovation solutions

3.Name of the Laboratory: Centre for Automation & Robotics (**Sponsored by AICRA**)

Program Outcomes (Pos)	Program Specific Outcomes (PSOs)	Sustainability Development Goal (SDG)
PO1, PO2, PO3, PO4, PO5, PO6, PO7, PO8, PO9, PO10, PO11, PO12	PSO1, PSO2	SDG 4, SDG 9, SDG 12, SDG 13

The Centre for Automation & Robotics, sponsored by AICRA, is established to provide students with hands-on exposure to industrial automation, robotics, intelligent systems, and emerging technologies. The centre facilitates practical learning through training, workshops, demonstrations, and project-based activities involving robotic systems, sensors, actuators, PLCs, automation technologies, programming, and control systems. It promotes innovation, problem-solving, interdisciplinary learning, and industry-oriented skill development, enabling students to apply theoretical concepts to real-world engineering applications.

Products Developed:

- Edge AI-Driven Smart Pipeline Inspection Robot
- Solar Powered Smart Aquatic Environment Management System For Pollution Control
- Precision farming Rover with automated weed cutting

Expected Outcomes

· Student projects addressing **manufacturing, logistics, healthcare, agriculture and smart-campus applications**.

· Participation in **hackathons, project exhibitions and innovation competitions** using developed prototypes.

· Integration of **IoT, AI/ML, computer vision and robotics** for intelligent automation solutions.

4.Name of the Centre of Excellence: Athes Defence Centre of Excellence

Program Outcomes (POs)	PSO	Sustainability Development Goal (SDG)
PO1, PO2, PO3, PO4, PO5, PO8, PO9, PO10, PO11	PSO1, PSO2	SDG 4, SDG 8, SDG 9

The **Athes Centre of Excellence in Defence Technology** was successfully inaugurated on 6th March 2026 Sri Krishna College of Engineering and Technology, Coimbatore. The establishment of this centre marks an important milestone in strengthening industry–academia collaboration in the field of defence technology and innovation.



5. Lab NAme : Virtusa CoE – JAVA FULL STACK

The CoE offers structured training in Software Engineering, Core Java, JDBC, SQL, HTML, CSS, JavaScript, React, Spring Boot, REST services, Hibernate, JPA, and software design patterns. The curriculum integrates theoretical learning with hands-on programming, application development, and project-based learning. Students gain practical exposure to full-stack application development, enabling them to understand and implement both front-end and back-end technologies.

The CoE has contributed to student employability through systematic training, technical assessments, and industry engagement. Faculty development is another key component of the CoE. Faculty members have participated in Virtusa's Train-the-Trainer programmes and technology orientation sessions on Java Full Stack Development, Design Patterns, Advanced Java, Angular, React, Spring Boot, Hibernate, and Maven.

Program Outcomes (POs)	PSO	Sustainability Development Goal (SDG)

PO1, PO2, PO3, PO4, PO5, PO8, PO9, PO10, PO11	PSO1, PSO2	SDG 4, SDG 8, SDG 9



Expected Outcome:

The Virtusa Centre of Excellence has enhanced industry readiness through practical Java Full Stack training, hands-on projects, technical assessments and industry interaction. Students are benefited by developing skills in modern full-stack technologies, while faculty members strengthened their technical expertise through industry-led training and hands-on projects. The CoE has effectively bridged the gap between academic learning and industry requirements

Lab 6: Centre of Excellence in VLSI Design and Semiconductor Technology - Chip to startup

The **Centre of Excellence in VLSI Design and Semiconductor Technology** provides a strong foundation for developing a comprehensive academic and research ecosystem at SKCET.

The laboratory can support:

- Advanced VLSI laboratory courses
- Student mini-projects and major projects
- Final-year engineering projects
- Postgraduate projects and research
- Faculty development programmes
- Hands-on technical workshops
- Industry-oriented training
- Student certification programmes
- Semiconductor research
- Industry-academia collaborative projects
- Innovation and entrepreneurship activities
- Consultancy and sponsored research

The facility can also serve as a platform for interdisciplinary collaboration among **Electronics and Communication Engineering, Electrical and Electronics Engineering, Computer Science and Engineering, Artificial Intelligence and Data Science, Information Technology, and other related disciplines**.

Expected outcome

The **Centre of Excellence in VLSI Design and Semiconductor Technology – Advanced EDA & VLSI Design Laboratory** at **Sri Krishna College of Engineering and Technology, Coimbatore**, represents a significant advancement in the institution's commitment to high-quality engineering education, research, innovation, and industry-oriented skill development.

With **high-performance computing infrastructure** and access to advanced EDA tools from **Synopsys, Ansys, Keysight, Xilinx, and Silvaco**, enabled through the **ChipIN Centre of C-DAC, under the Ministry of Electronics and Information Technology (MeitY), Government of India**, the laboratory provides a strong platform for students and faculty to explore modern electronic and semiconductor technologies.

The unique combination of **digital VLSI, ASIC design, FPGA implementation, RF and microwave simulation, electromagnetic analysis, semiconductor device modelling, and advanced EDA workflows** enables ECE students to obtain a broader understanding of the electronics and semiconductor ecosystem.

Most importantly, the facility enables students to gain practical exposure to **front-end and back-end VLSI design**, while also providing opportunities to explore **RF systems, communication technologies, FPGA-based hardware, semiconductor devices, and emerging electronic technologies**.

Through effective integration of these resources into academic courses, laboratory sessions, student projects, research activities, technical training, and industry collaboration, SKCET aims to nurture **technically skilled, innovative, research-oriented, and industry-ready engineers** capable of contributing to the rapidly evolving VLSI, electronics, and semiconductor technology ecosystem.

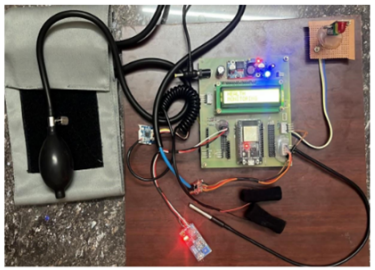
Lab 7: AICTE Idea Lab

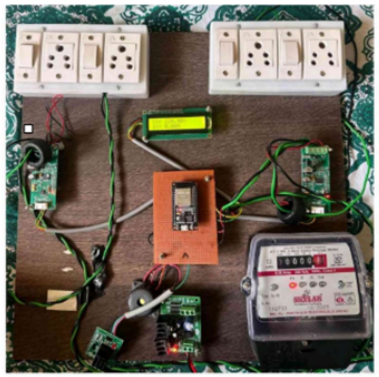
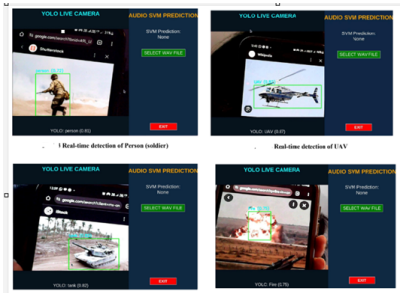
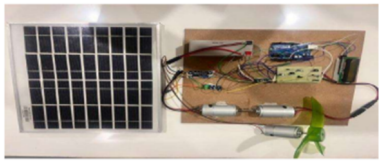
SKCET is honoured to establish Prestigious AICTE Idea Lab. This Idea lab will enhance the product making skills of the students and empower them to pursue the path of innovation and entrepreneurship. Also skill development programs like bootcamp, ideathon workshop, hackathon, technical exhibition, etc will be conducted to train the local community which includes school students and professionals.

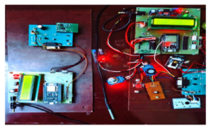
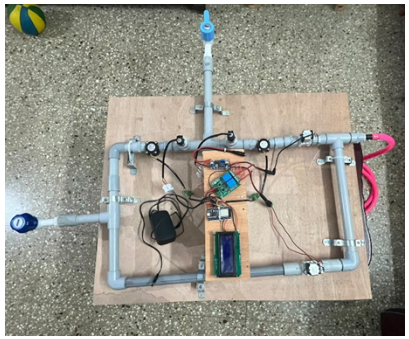
Key features of the lab establishment includes

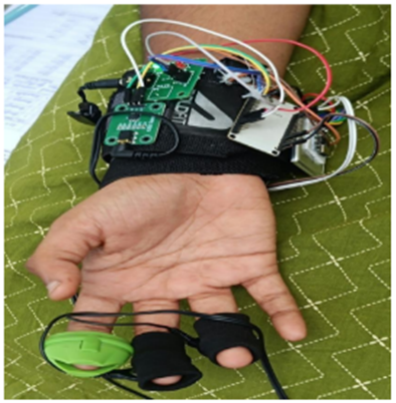
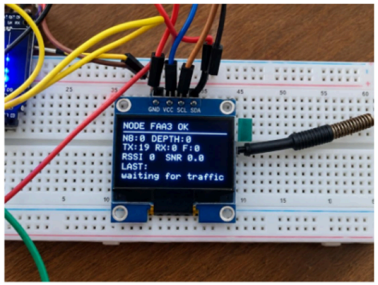
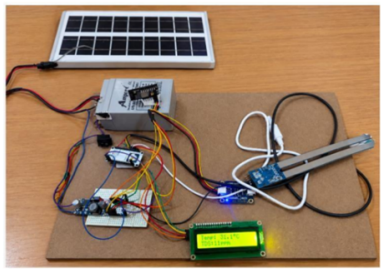
- Promotion of Innovation and Creativity
- Hands-on Learning
- Entrepreneurship Support
- Enhancing Employability
- Collaborative Learning Environment
- Access to Advanced Equipment
- Support for Social Innovation
- Product Commercialization and Technology Transfer

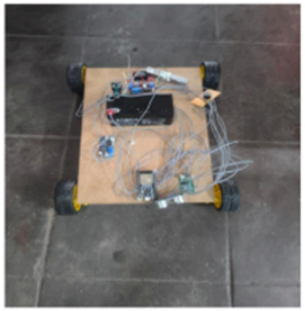
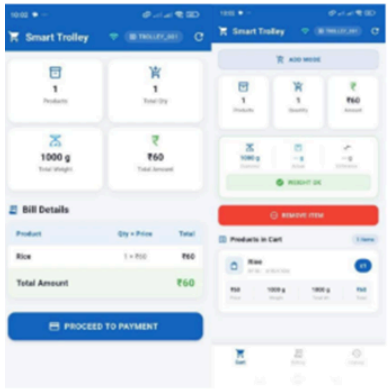
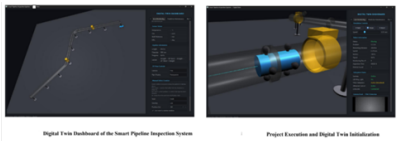
7.5.4 Outcome of Project Laboratory / Research Laboratory / Centre of Excellence

S.No	Domain	Project Title	Prototype	SDG
1.	Digital Health	Adaptive CNN-NAS and Edge-IoT Framework for Facial Emotion Recognition and Personalized Psychological Monitoring for Therapeutic Decision Support		3,9,10,17
2.	Precision Farming with Robotics	Precision farming Rover with automated weed cutting	 	2,9,12,13

3.	Smart Energy Saving	Smart Energy Meter with AI-Based Predictive Analysis and Remote Monitoring	 	7,9,12,13
4.	Security & Surveillance	Intrusion threat detection in hostile terrains using YOLOv4-TINY		9,11,16
5.	Renewable Energy Systems	Smart hybrid renewable energy harvesting and LSTM based predictive demand management for improved industrial efficiency		7,9,12,13

6.	Intelligent Security Systems	Smart Gate Control using deep learning and license plate recognition	 License Plate detection using YOLOv8	9,11,16
7.		Real time sign language Recognition system for Deaf and Dumb using AIML		4,8,9,10
8.	Industrial Safety	Smart Helmet for Next Gen Industrial safety and Coal Mines	 	3,8,9,12
9.	IoT & Smart Sensors	Smart Water Pipeline Defect Detection System using Artificial Intelligence and Machine Learning (AI/ML)		6,9,11,12

10.	Wearable Devices	Advanced Wearable Devices-Based Gaming System for Physical and Mental Therapy		3,4,9,10
11.	Internet of Things	TinyML-Based Predictive Energy Management and Service Prioritization in LoRa Disaster Mesh Networks		9,11,13
12.	IoT-based Environmental Monitoring	Solar Powered Smart Aquatic Environment Management System For Pollution Control		6,7,9,13,14

13.	Autonomous Robotics	AprilTag-Based Smart Autonomous Shopping Trolley	 Trolley Setup  Mobile App	9,11,12
14.	Autonomous Robotics	Edge AI-Driven Smart Pipeline Inspection Robot		6,8,9,12

PART E: First Year faculty and financial Resources

(Data to be filled in for the first year course faculty and budget allocation and utilization)

E1. First Year Student-Faculty Ratio (FYSFR)

Table No. E1.1: FYSFR details.

Year	Sanctioned intake of all UG programs (S4)	No. of required faculty (RF4= S4/20)	No. of faculty members in Basic Science Courses & Humanities and Social Sciences including Management courses (NS1)	No. of faculty members in Engineering Science Courses (NS2)	Percentage= No. of faculty members ((NS1*0.8) + (NS2*0.2))/(No. of required faculty (RF4)); Percentage= ((NS1*0.8) +(NS2*0.2))/RF
2023-24(CAYm2)	1651	83	72	72	87
2024-25(CAYm1)	1845	92	73	81	81
2025-26(CAY)	3612	181	121	261	82

E2. Budget Allocation, Utilization, and Public Accounting at Institute Level

Table No. E2.1: Budget and actual expenditure incurred at Institute level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Infrastructure Built-Up	400000000	396937329	290000000	229936923	122051500	132433690	18862500	15520095
Library	12000000	12410604	2500000	899191	9540000	8956320	1200000	1139982
Laboratory equipment	33000000	32636797	20000000	11964014	16570000	5445518	10000000	11805745

Teaching and non-teaching staff salary	380000000	376756807	231750000	225792575	216520000	216419721	220300000	218340926
Outreach Programs	2000000	2065527	1855000	1987948	2000000	1828871	47500000	12682720
R&D	14000000	13784366	7445000	7062415	4000000	3612855	5850000	4473819
Training, Placement and Industry linkage	10000000	10689402	9360000	9780383	20500000	20496882	22000000	21216844
SDGs	75000000	75967378	60785000	58087747	55000000	50103073	71025395	48119945
Entrepreneurship	600000	525689	588000	568187	150000	331229	150000	884144
Others, specify	260000000	263139084	225714000	291036938	234124825	235936395	153874500	207804812
Total	**1186600000**	**1184912983**	**849997000**	**837116321**	**680456325**	**675564554**	**550762395**	**541989032**

E3. Budget Allocation, Utilization, and Public Accounting at Program Specific Level

Table No. E3.1: Budget and actual expenditure incurred at program level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Laboratory equipment	2650000	2598360	400000	396675	700000	616650	700000	707504
Software	320000	305666	125000	119666	300000	354000	150000	194458
SDGs	1020300	1019921	819351	592900	760381	692734	1079652	704727
Support for faculty development	170000	163000	350000	296000	250000	242000	180000	253500
R & D	1700000	1798593	3300000	3511682	2500000	2492585	700000	704405
Industrial Training, Industry expert, Internship	120000	121285	100000	80572	100000	75572	10000	16000
Miscellaneous Expenses*	1100000	1020333	300000	353314	400000	466989	1000000	1170996
Total	**7080300**	**7027158**	**5394351**	**5350809**	**5010381**	**4940530**	**3819652**	**3751590**